

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Hazuki OKABAYASHI et al. Group Art Unit : 2185
Appl. No. : 10/577,133 (U.S. National Stage of Examiner : A.P. Salva
PCT/JP04/16272)
I.A. Filed : November 2, 2004 Confirmation No. : 8197
For : CACHE MEMORY AND METHOD FOR CACHE ENTRY
REPLACEMENT BASED ON MODIFIED ACCESS ORDER

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Issue Fee
Randolph Building
401 Dulany Street
Alexandria, VA 22314

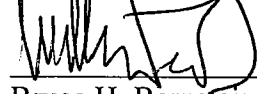
Sir:

In response to the Notice of Allowability, mailed by the U.S. Patent and Trademark Office on December 29, 2010 and to the Statement of Reasons for Allowance attached thereto, Applicants wish to clarify the record with respect to the basis for the patentability of claims in the present application.

In this regard, while Applicants do not disagree with the Examiner's indication of allowability, Applicants submit that each of the claims in the present application recites a combination of features, and that the basis for patentability of each of these claims is based on the combination of features recited therein.

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,
Hazuki KAWAI et al.


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March 29, 2011
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